

- PROGRAMMABLE DEVICES ARE TO BE LOADED WITH THE FIRMWARE FILES AS SHOWN ON THE CHART VIA THE LISTED "JTAG" CONNECTOR.
- ONCE PROGRAMMED, PLACE THE FIRMWARE VERSION LABEL ON THE COMPONENT SIDE IN THE APPROXIMATE LOCATION SHOWN. THIS LABEL IS TO LIST THE PROGRAMMED NXG SYS I/O PART NUMBER AS WELL AS THE CURRENT REVISION OF THIS DRAWING.

PROGRAMMED NXG SYS I/O P/N	U85 PROGRAM FILE VIA JTAG CONN. P5	BASE BOARD
A1A10000423.00	A1A18000509.101 REV AE Ver 2.12	A1A461X92.00

NOTE: COMPONENT U85 HAS BEEN STRUCTURED IN SUCH A WAY THAT IT IS NOW A PREPROGRAMMED PART. IT IS INCLUDED UNDER THE STRUCTURE FOR SIEMENS PART A1A890204.100 , EPLD DEVICE PROGRAM (LATEST VERSION). THIS DRAWING IS FOR LEGACY REFERENCE.

REV	NO	DESCRIPTION
A	11184	CHANGES MADE PER REV. 10/19/05
B	11158	NO DIFFERENCE
C	11492	REV. BOARD ARE REVISED TO ADD A REV. C BOARD (BASE) AND UPDATED WITH THE BOARD (REV. 11/19/05)
D	11767	REV. BOARD ARE REVISED TO ADD A REV. C BOARD (BASE) AND UPDATED WITH THE BOARD (REV. 11/19/05)
E	11970	THIS REV. IS ONLY USED FOR THE BOARD (REV. 11/19/05) AND OUT IN THE FIELD. THIS REV. IS ONLY USED FOR THE BOARD (REV. 11/19/05) AND OUT IN THE FIELD.
F	11970	THIS REV. IS ONLY USED FOR THE BOARD (REV. 11/19/05) AND OUT IN THE FIELD. THIS REV. IS ONLY USED FOR THE BOARD (REV. 11/19/05) AND OUT IN THE FIELD.
G	12083	REVISED TO ADD A REV. C BOARD (BASE) AND UPDATED WITH THE BOARD (REV. 11/19/05)
H	13836	REVISED TO ADD A REV. C BOARD (BASE) AND UPDATED WITH THE BOARD (REV. 11/19/05)
I	14885	REVISED TO ADD A REV. C BOARD (BASE) AND UPDATED WITH THE BOARD (REV. 11/19/05)
J	15214	REVISED TO ADD A REV. C BOARD (BASE) AND UPDATED WITH THE BOARD (REV. 11/19/05)
K	15214	REVISED TO ADD A REV. C BOARD (BASE) AND UPDATED WITH THE BOARD (REV. 11/19/05)
L	15214	REVISED TO ADD A REV. C BOARD (BASE) AND UPDATED WITH THE BOARD (REV. 11/19/05)
M	15214	REVISED TO ADD A REV. C BOARD (BASE) AND UPDATED WITH THE BOARD (REV. 11/19/05)
N	15214	REVISED TO ADD A REV. C BOARD (BASE) AND UPDATED WITH THE BOARD (REV. 11/19/05)
O	15214	REVISED TO ADD A REV. C BOARD (BASE) AND UPDATED WITH THE BOARD (REV. 11/19/05)
P	15214	REVISED TO ADD A REV. C BOARD (BASE) AND UPDATED WITH THE BOARD (REV. 11/19/05)
Q	15214	REVISED TO ADD A REV. C BOARD (BASE) AND UPDATED WITH THE BOARD (REV. 11/19/05)
R	15214	REVISED TO ADD A REV. C BOARD (BASE) AND UPDATED WITH THE BOARD (REV. 11/19/05)
S	15214	REVISED TO ADD A REV. C BOARD (BASE) AND UPDATED WITH THE BOARD (REV. 11/19/05)
T	15214	REVISED TO ADD A REV. C BOARD (BASE) AND UPDATED WITH THE BOARD (REV. 11/19/05)
U	15214	REVISED TO ADD A REV. C BOARD (BASE) AND UPDATED WITH THE BOARD (REV. 11/19/05)
V	15214	REVISED TO ADD A REV. C BOARD (BASE) AND UPDATED WITH THE BOARD (REV. 11/19/05)
W	15214	REVISED TO ADD A REV. C BOARD (BASE) AND UPDATED WITH THE BOARD (REV. 11/19/05)
X	15214	REVISED TO ADD A REV. C BOARD (BASE) AND UPDATED WITH THE BOARD (REV. 11/19/05)
Y	15214	REVISED TO ADD A REV. C BOARD (BASE) AND UPDATED WITH THE BOARD (REV. 11/19/05)
Z	15214	REVISED TO ADD A REV. C BOARD (BASE) AND UPDATED WITH THE BOARD (REV. 11/19/05)

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SCALE	NTS	LAYER	
DATE	8-18-05	DWG NO	A1AD10000423
DRW SIZE		REV	BN
DRW KMO		CAD FILE	PCAD2004
CUSTOMER	MV	SHEET NO	1 OF 2
ENGR	V. McMichael	STANDARD	

SIEMENS
Industries, Inc.
Drive Technologies
Large Drives Applications

TITLE ASSEMBLY
PCA W/FIRMWARE, NXG SYS I/O
10000423.00

WARNING
HAZARD OF ELECTRICAL SHOCK
DISCONNECT INCOMING POWER BEFORE OPENING OR WORKING ON THIS UNIT.

ESD SENSITIVE EQUIPMENT

THIS SHEET ADDED FOR REFERENCE ONLY

PART NUMBER : 10000423.00
 NAME: NXG System IO Board, Programmed
 DESCRIPTION: Printed circuit assembly w/firmware; NXG System IO; ISA-AT form factor
 FUNCTION: Provides coordinated system feedback and general purpose isolated input and output signals.

Electrical Specifications:

Power:

Input: 5Vdc, 1A; +12Vdc, 250mA;
 -12Vdc, 200mA; 24Vdc, 1.4A

Output: Not applicable

Signals:

Input: ISA bus, TTL; Feedback, +/-10V(x13), 100mA(x5); Fsamp stobe, TTL(x1); Encoder, Quadrature Differential Isolated HTL(x2); Fault circuits, TTL(x2), +/-15Vdc present(x3); CR3(contact); IO, 24Vdc(x20) logic, 24Vdc present(x1), 0-10V/4-20mA(x2), 4-20mA(x1) analog.

Output: ISA bus, TTL, -5Vdc; IOC (0-10V Rectified composite of output current); CR3, 5Vdc; Fault circuits, TTL (x2); IO, 24Vdc(x16), 4-20mA(x2) analog.

Connections: P9 & P8 - ISA bus; P4 - Feedback; P2 - Isolated digital IO; P1 - Isolated analog IO; P3 - to modulator board; P6 - Alt power +/-12Vdc, 5Vdc.

Electrical Specifications Cont'd. :

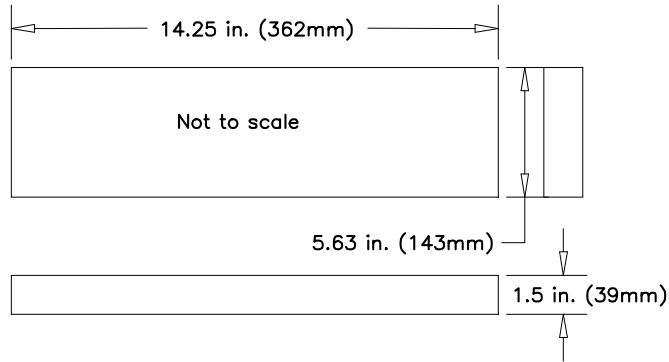
Environment :

Temperature: Operating 0 ~ 50 deg. C,
 Storage -40 ~ 85 deg. C.

Humidity: Operating 20 ~90% non-condensing
 Storage 10 ~ 95%.

Other:

Overall Physical Envelope Dimensions



Mass/Weight: 298 grams;
 10.5 ounces



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			LAYER		DWG.NO. A1AD10000423	REV BN	
			DATE 10-23-06		CUSTOMER MV	CAD FILE PCAD2004	SHEET NO. 2 OF 2
			DWGSZ B		CUST.ORDER STANDARD		
			DFTR H.BOEHM				
			ENGR V. McMICHAEL				

A B C D E F G H